



2661 #5
11/01/02

PATENT
Attorney's Docket No. MP0117

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Pannell, Donald et al.

Group Art Unit: 2661

Serial No.: 10/071417

Examiner: Not Yet Assigned

Filed: February 6, 2002

Title: Quality of Service Queueing System for a Network Switch

RECEIVED

OCT 30 2002

Technology Center 2600

CERTIFICATE OF MAILING

I hereby certify that this correspondence, and the documents attached hereto, are being deposited with the United States Postal Service as First Class mail in an envelope addressed to Commissioner for Patents, Washington, D.C. 20231 on this date.

Date: 10/22/02

Suzanne Forno
Suzanne M. Forno

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

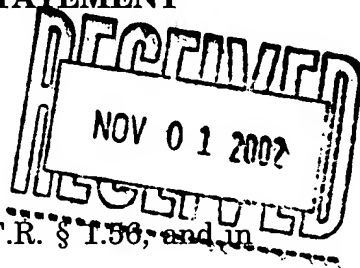
Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.96, and in accordance with the practice under 37 C.F.R. § 1.97 and § 1.98, the Examiner's attention is directed to the documents listed on the enclosed Form PTO-1449. Copies of the listed information are also enclosed.

This Information Disclosure Statement is being filed within three months of the U.S. filing date or before the mailing date of a first Office Action on the merits. No certification or fee is required (37 CFR §1.97(b)).

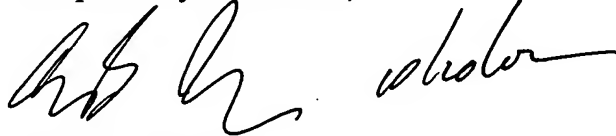
CONCLUSION

The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any over-payment to deposit account 50-1236.



It is respectfully requested that the above information be considered by the Examiner and that a copy of the enclosed Form PTO-1449 be initialed and returned indicating that such information has been considered.

Respectfully submitted,



Eric B. Janofsky
Attorney for Applicants
Registration No. 30759

Please address all correspondence to:

Eric B. Janofsky
General Patent Counsel
Marvell Semiconductor, Inc.
700 First Avenue, Mail Stop 509
Sunnyvale, CA 94089
General Telephone Number (408) 222-2500
Facsimile (408) 752-9034
Email: sforno@marvell.com
Customer No. 23624

Date: 

SUPPLEMENTAL INFORMATION DISCLOSURE
CITATION

USE SEVERAL SHEETS IF NECESSARY

PAGE 1 OF 1

APPLICANT

Pannell, Donald et al.

FILING DATE

Herewith

GROUP:

2661

U.S. PATENT DOCUMENTS

E.I.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLAS S	FILING DATE
AA	5541912	7/30/96	Choudhury et al.			10/4/94
AB						
AC						
AD						
AE						
AF						
AG						
AH						
AI						
AJ						
AK						

RECEIVED

OCT 30 2002

Technology Center 2600

FOREIGN PATENT DOCUMENTS

E.I.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLAS S	TRANS- LATION
AL						
AM						
AN						
AO						
AP						
AQ						

OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

AR	Abhijit K. Choudhury and Ellen L. Hahne, "Dynamic Queue Length Thresholds for Shared-Memory Packet Switches," Bell Laboratories, Holmdel, NJ, IEEE/ACM Transactions on Networking, Vol. 6, No. 2, April 1998, pp.130-140
AS	
AT	
EXAMINER	
DATE CONSIDERED	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.